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ABSTRACT

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The present invention relates to a method and a system for generating a global simulation model of an architecture. In particular, the invention relates to a method of automatic generation, by means of a data processing system associated with a program called a Configurator for creating a global simulation model of an architecture comprising models of integrated circuits under development, comprising:

- reading an architecture description file of the global model and storing information related to all of the possible configurations;
- instantiating the components and storing the corresponding information in an instance connection table;
- topologically connecting the interface signals,
- physically connecting the interface signals, at the level of each instance of the components using a component and connection rule table, and storing the corresponding information in a wiring table; and
- automatically generating the HDL-type and HLL-type source files of the global simulation model, corresponding to the configuration specified by the configuration definition file.